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Military Communications Conference, 1990. MILCOM '90, Conference Record, New Era'. 1990 IEEE , 30 Sept.-3 Oct. 1990

Pages:254 - 257 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) IEEE CNF
2 Hardware implementation of a memory allocator*Jasrotia, K.; Jianwen Zhu;*

Digital System Design, 2002. Proceedings. Euromicro Symposium on , 4-6 Sep 2002

Pages:355 - 358

[\[Abstract\]](#) [\[PDF Full-Text \(330 KB\)\]](#) IEEE CNF
3 68040 memory modules and bus controller*Martin, B.; McMahan, S.; Sood, L.;*

Computer Design: VLSI in Computers and Processors, 1990. ICCD '90.

Proceedings., 1990 IEEE International Conference on , 17-19 Sept. 1990

Pages:179 - 182

[\[Abstract\]](#) [\[PDF Full-Text \(324 KB\)\]](#) IEEE CNF
4 HARP: an open architecture for parallel matrix and signal processing*Dowling, E.M.; Fu, Z.; Drafz, R.S.;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 4 , Issue: 10 , Oct. 1993

Pages:1081 - 1091

[\[Abstract\]](#) [\[PDF Full-Text \(1004 KB\)\]](#) IEEE JNL

5 A 1.71 M-transistor CMOS CPU chip with a testable cache architecture
Saito, Y.; Shimazu, Y.; Kobayashi, S.; Shimizu, T.; Matsuo, M.; Ohtsuka, A.; Shirai, K.; Murata, H.; Nishiwaki, Y.; Fujioka, I.; Nabeta, Y.; Kanamoto, H.; Hiraoka, S.; Suzuki, T.; Hinata, J.; Shimotsuma, Y.;
 Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC 1993 IEEE International , 24-26 Feb. 1993
 Pages:86 - 87, 266

[\[Abstract\]](#) [\[PDF Full-Text \(512 KB\)\]](#) IEEE CNF

6 Software-only bus encoding techniques for an embedded system
Wei-Chung Cheng; Jian-Lin Liang; Pedram, M.;
 Design Automation Conference, 2002. Proceedings of ASP-DAC 2002. 7th Asia South Pacific and the 15th International Conference on VLSI Design. Proceedings , 7-11 Jan. 2002
 Pages:126 - 131

[\[Abstract\]](#) [\[PDF Full-Text \(305 KB\)\]](#) IEEE CNF

7 The 68040 processor. I. Design and implementation
Edenfield, R.W.; Gallup, M.G.; Ledbetter, W.B., Jr.; McGarity, R.C.; Quintana, Reininger, R.A.;
 Micro, IEEE , Volume: 10 , Issue: 1 , Feb. 1990
 Pages:66 - 78

[\[Abstract\]](#) [\[PDF Full-Text \(1108 KB\)\]](#) IEEE JNL

8 A five volt only Advanced Integrated Multiplexed Hybrid (AIM-HY)
Friedman, S.N.;
 Aerospace and Electronics Conference, 1989. NAECON 1989., Proceedings of the IEEE 1989 National , 22-26 May 1989
 Pages:50 - 52 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) IEEE CNF

9 An embedded 32-b microprocessor core for low-power and high-performance applications
Clark, L.T.; Hoffman, E.J.; Miller, J.; Biyani, M.; Luyun Liao; Strazdus, S.; Mori, M.; Velarde, K.E.; Yarch, M.A.;
 Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 11 , Nov. 2001
 Pages:1599 - 1608

[\[Abstract\]](#) [\[PDF Full-Text \(327 KB\)\]](#) IEEE JNL

10 ASIC implementation of a RISC microprocessor for portable workstations
Seung Ho Lee; Beoyng Yoon Choi; Moon Key Lee;
 TENCON '95. 1995 IEEE Region 10 International Conference on Microelectronics and VLSI , 6-10 Nov. 1995
 Pages:484 - 487

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) IEEE CNF

11 Implementing a carrier-band node using VLSI*Erickson, I.L.;*

Industrial Electronics, IEEE Transactions on , Volume: 35 , Issue: 1 , Feb. 198
Pages:1 - 5

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) IEEE JNL

12 Optoelectronic page-oriented database filter based on a single chip photonic VLSI design*Tang, J.; Lee, B.K.; Chi, R.C.J.; Steckl, A.J.; Beyette, F.R., Jr.;*

Electronic-Enhanced Optics, Optical Sensing in Semiconductor Manufacturing, Electro-Optics in Space, Broadband Optical Networks, 2000. Digest of the LEO Summer Topical Meetings , 24-28 July 2000
Pages:I71 - I72

[\[Abstract\]](#) [\[PDF Full-Text \(124 KB\)\]](#) IEEE CNF

13 System on Chip Age*Mori, K.; Yamada, H.; Takizawa, S.;*

VLSI Technology, Systems, and Applications, 1993. Proceedings of Technical Papers. 1993 International Symposium on , 12-14 May 1993
Pages:K15 - K20

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) IEEE CNF

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